



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application of

Inventor/s: DALLY et al.

Appln. No: 09/871,301

Filed: May 30, 2001

FOR: *System and Method for Performing Efficient  
Conditional Vector Operations for Data  
Parallel Architectures*



Atty. Dkt.: 081013/0269932

Group Art Unit: 2183

Examiner: Unknown

Date: August 10, 2001

**INFORMATION DISCLOSURE STATEMENT**

Asst. Commissioner for Patents  
Washington, D.C. 20231

Sir:

Enclosed herewith is Form PTO-1449 and the references cited therein.

This IDS is intended to be in full compliance with the rules, but should the Examiner find any part of its required content to have been omitted, prompt notice to that effect is earnestly solicited, along with additional time under Rule 97(f), to enable Applicant to comply fully. Copy of the article, "*Efficient Conditional Operations, for Data-parallel Architectures*," Computer Systems Laboratory, Stanford University, (2000), is enclosed only. The remaining references cited in the enclosed PTO-1449 were cited by the Examiner in the parent application, 09/152,944, filed September 14, 1998.

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed Form PTO-1449 with the Examiner's initials in the left column pursuant to MPEP 609 are earnestly solicited along with an early action on the merits.

Respectfully submitted  
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*I certify that this paper is being deposited with the U.S. Postal Service as first class mail in an envelope addressed to: Asst. Commissioner for Patents, Washington, D.C. 20231, on August 10, 2001.*

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Jeanette Cooks